

# Future Manufacturing

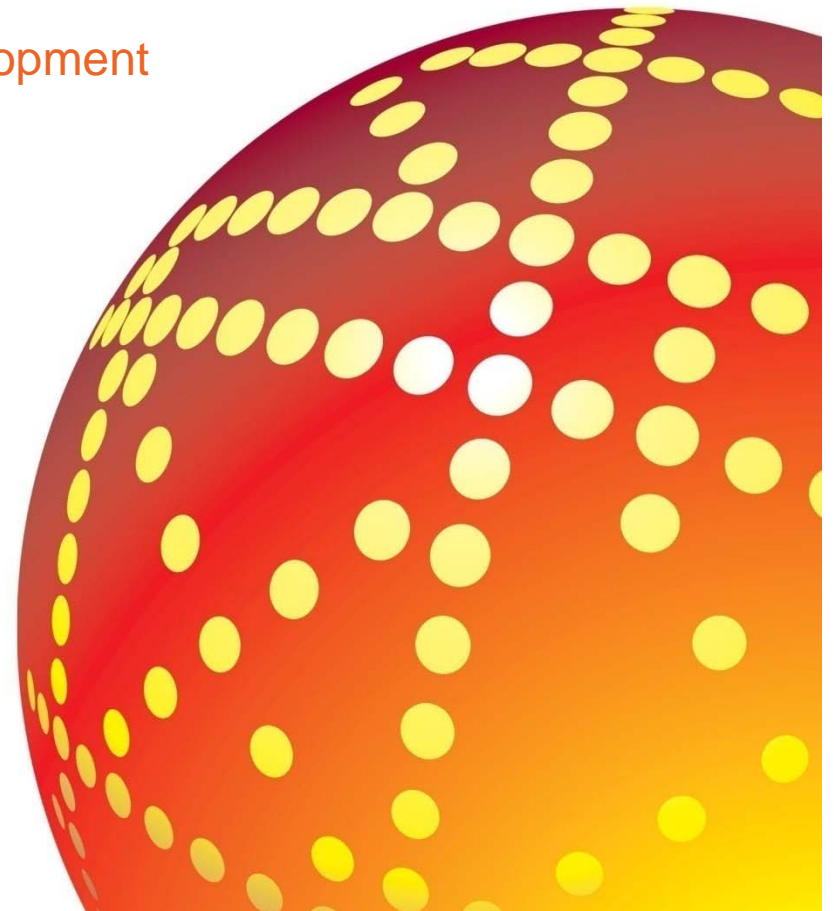
**Dirk Wristers**

Vice President of Technology Research & Development  
GLOBALFOUNDRIES Fab 1, Dresden



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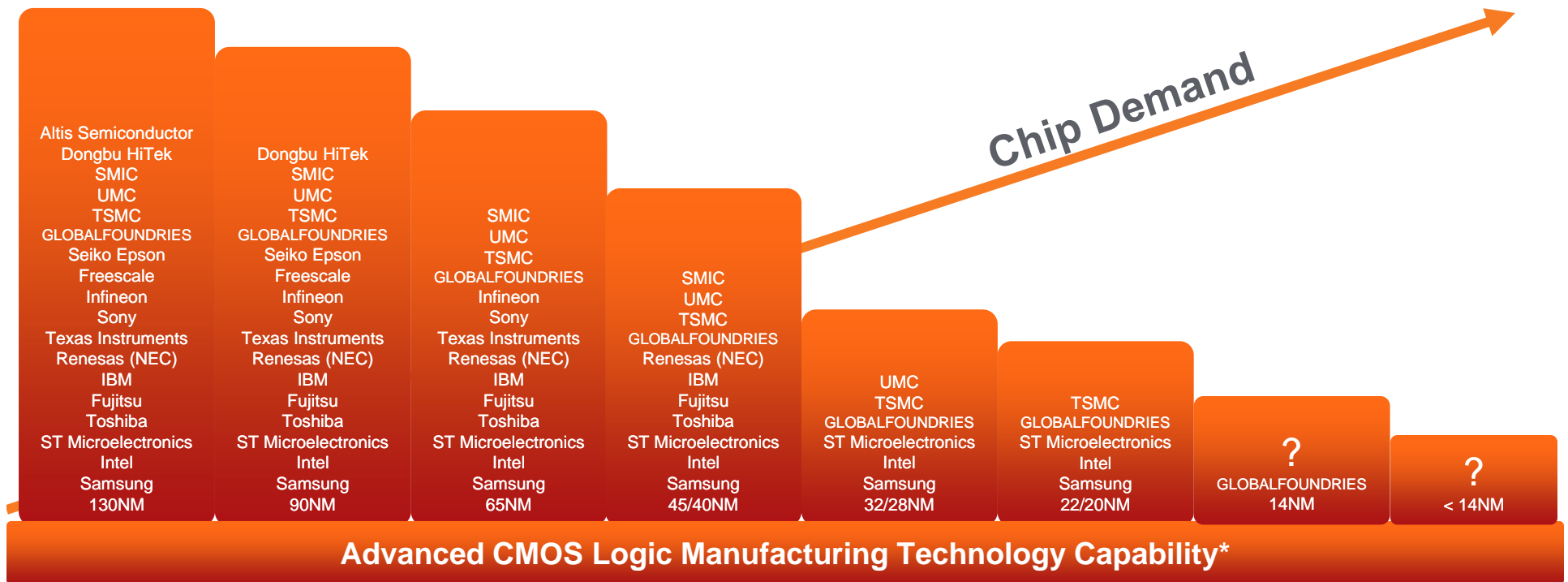




# When Physics and Finance conspire...

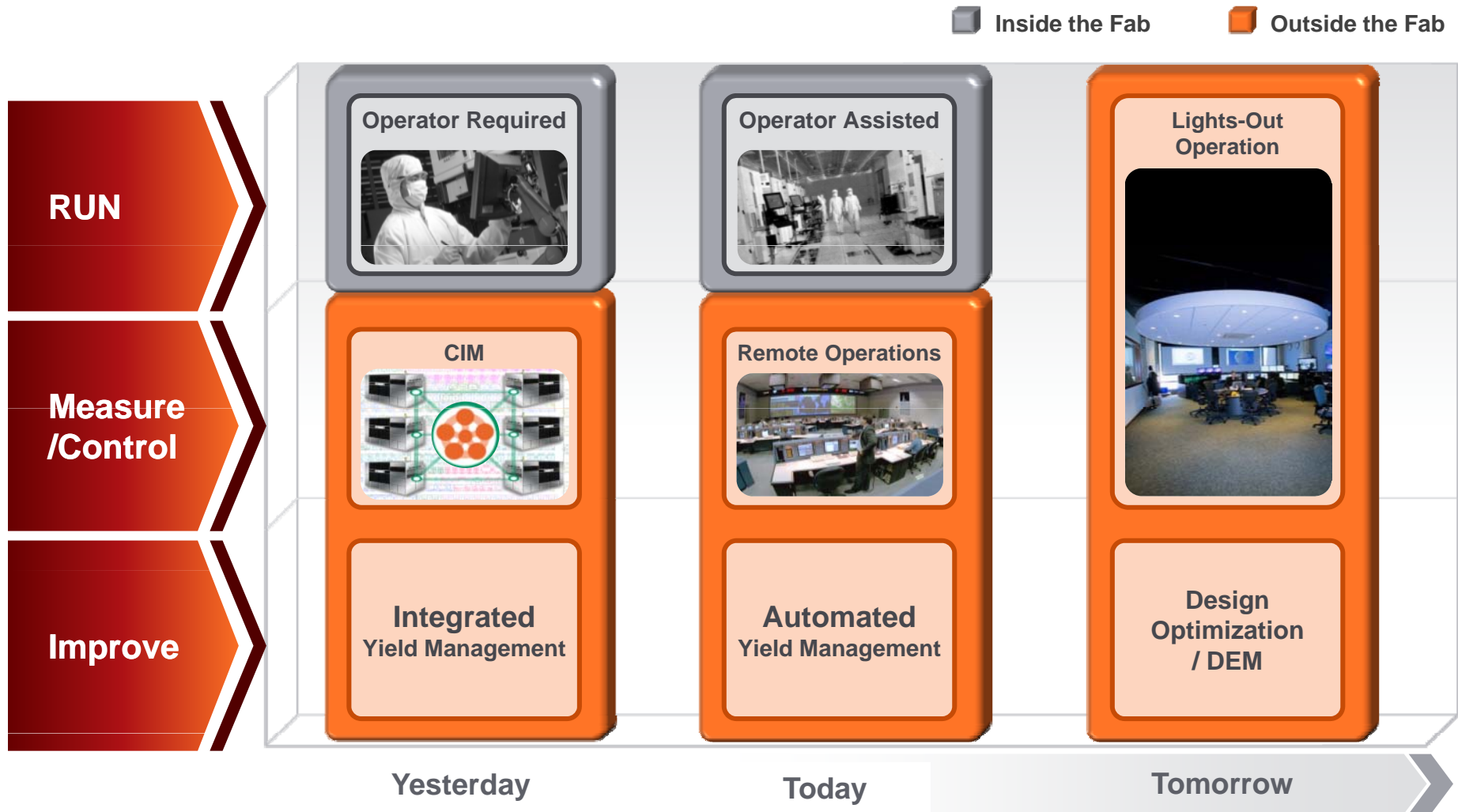
IDMs going fablite/fabless

Fabless % of overall semi revenue continues to increase





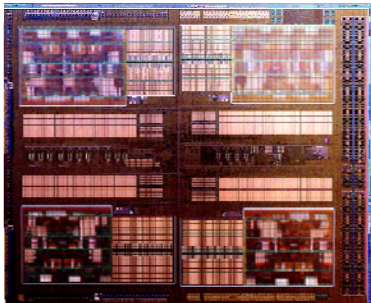
# Manufacturing Technology Vision





# What does it take?

## Innovation



## Technologies

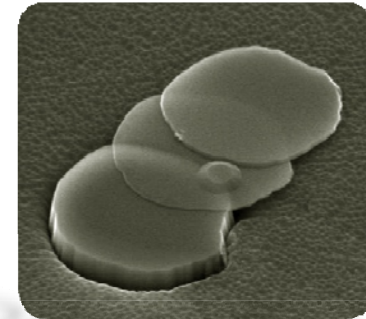
Comparing the options

	2D-SoC	3D-SiP	3D-TSV
Cost	Red	Green	Yellow
Performance	Yellow	Red	Green
Power	Green	Red	Green
Functionality	Green	Green	Green
Time to market	Red	Green	Green

STRENGTH WEARNESS

3D TSVs offer a cost-effective way to achieve high density and performance while also being able to integrate non-CMOS products with CMOS. The SEMATECH project is based on cost modeling to assure products will be manufacturable and affordable.

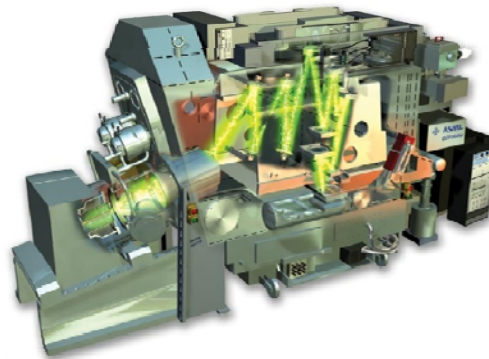
## Materials & Processes



## People



## Advanced Tools & Methods

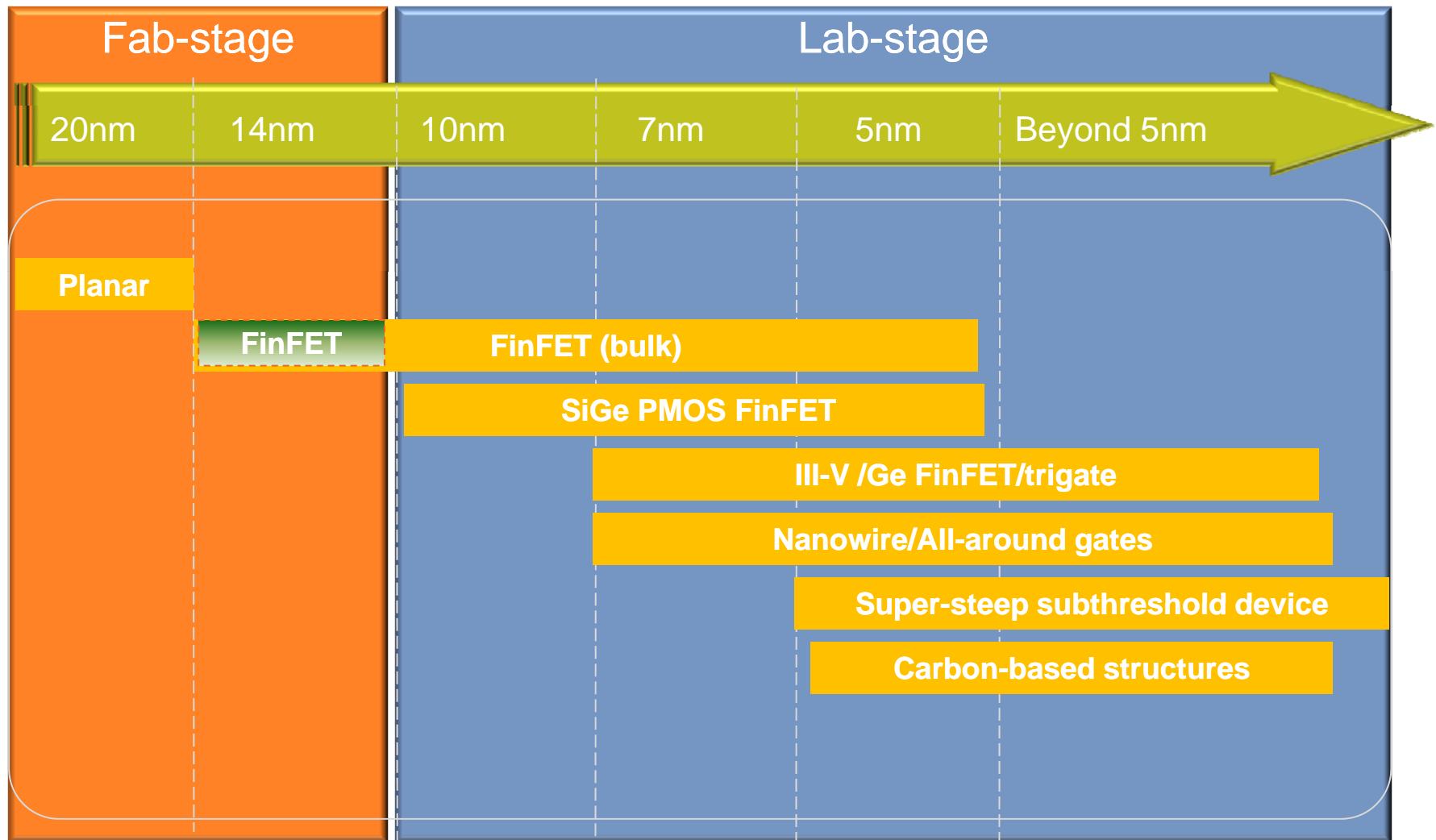


## Automation





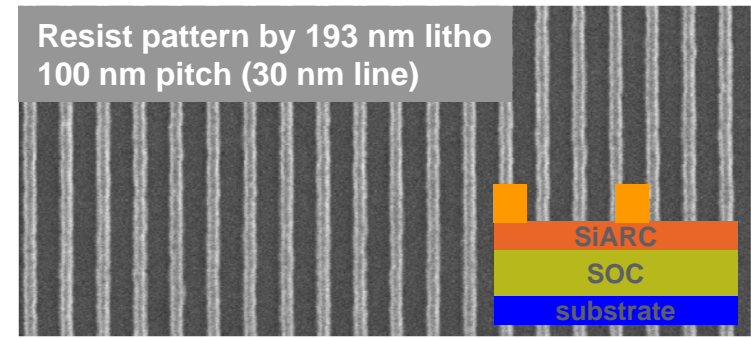
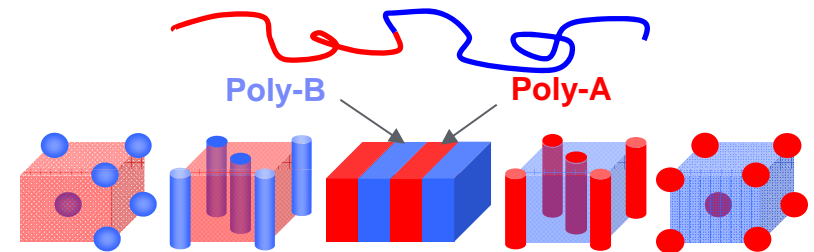
# New Materials & Processes in 'Fab & Lab'



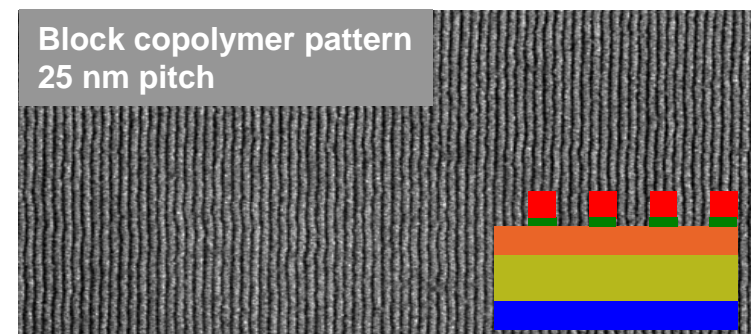


# Block Copolymer Directed Self Assembly (DSA)

- **Based on phase separation in block copolymer thin films**
  - Resolution controlled by block sizes
  - Feature size largely determined by polymer film
- **Benefits**
  - Very high ultimate resolution  $\ll 10$  nm
  - Can be implemented existing exposure tooling
  - Large multiples of frequency multiplication
- **Drawbacks**
  - New materials & processes
  - CD control, defectivity need work
  - Requires ultra-regular design



1. Apply neutral surface
2. Apply block copolymer
3. Etch







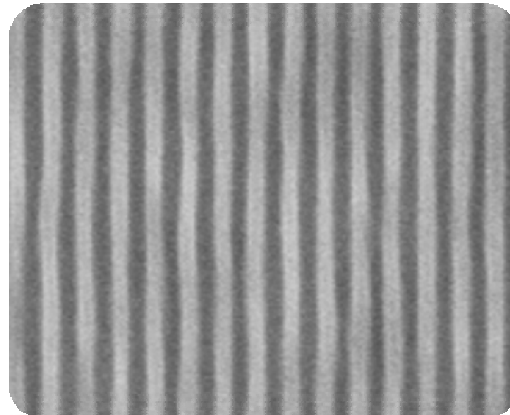
# EUV Technology: Comprehensive Solution

## Scanner Technology



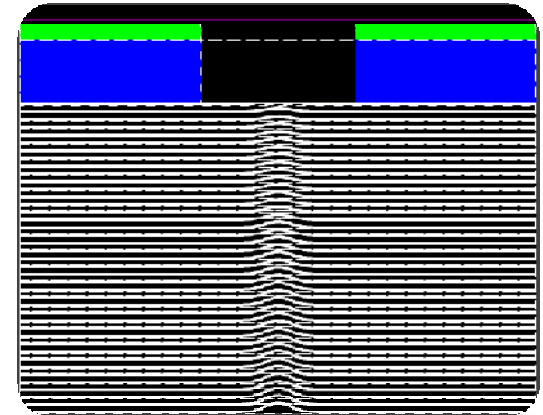
- *Scalable Source Power*
- *Reliability & Availability*

## Imaging Materials & Process Optimization



- *Sensitivity / Resolution / LER*
- *Integration of Thin Resists*

## Mask Technology

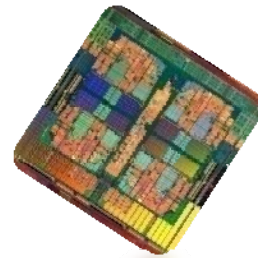


- *Inspection Infrastructure*
- *Mask Blank Supply Chain*

▪ Collaborative development and integration of these technology elements are required to bring EUV Lithography to maturation



# Manufacturing Evolution to Next Generation Factory



**APM 1.5**  
150mm Wafers  
Late 80's-1995

**Line Yield Improvements**

- Tool recipe verification
- Process monitoring
- Tool automation

**APM 2.0**  
200mm Wafers  
1995 - 2002

**Sort Yield Improvements**

- Tool recipe optimization
- Tool performance monitoring
- Process automation

**APM 3.0**  
300mm "Classic"  
2002 - 2009

**Sort Yield Optimization**

- Holistic, automated, integrated decision making
- Fab-wide control
- Tool-to-tool nonlinear material movement
- Process optimization
- Wafer/die level control

**NGF**  
300mm Prime  
2009 and still going strong

**Manufacturing Optimization**

- Lean manufacturing
- Small Lot Manufacturing
- Rapid Cycle Time
- Rapid product development
- Maximized ASP's
- Supply-chain automation
- On-demand manufacturing

**Future Fab**  
450 mm Prime  
**> 2017/18**

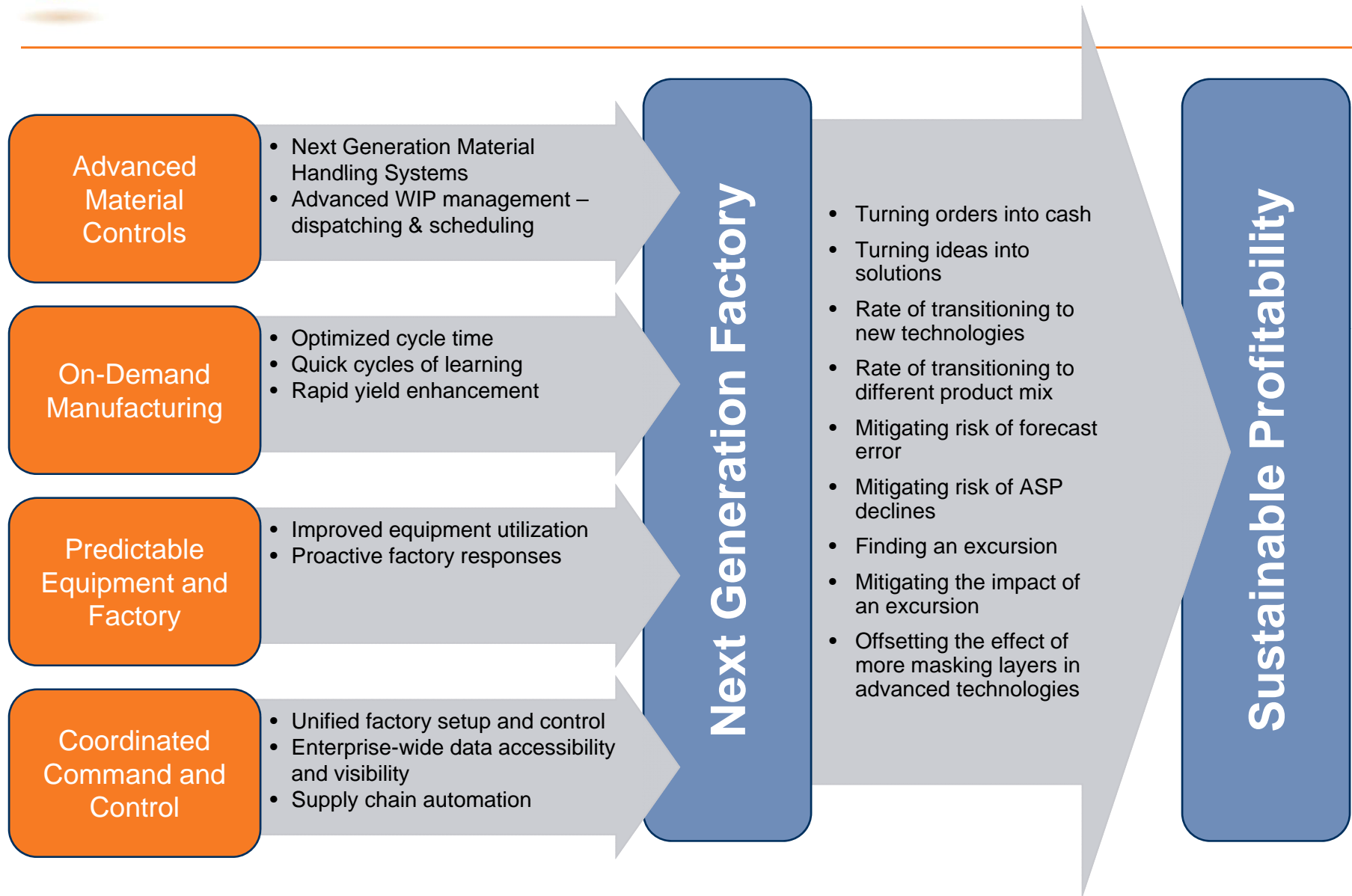
**Light-Out-Fab**

- Extreme Clean Dry Air
- Inert Conditions Clean Room (N2)





# Next Generation Factory





# Summary

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- Manufacturing in the electronics industry will evolve significantly moving forward as we are forced to deal with increase in cost as well as complexity.
- Mega-Fabs (> 1 million wafers/year) will leverage the scale of the investment required.
- “Lights-Out” Manufacturing will push system integration and automation to levels never before considered.



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# THANK YOU

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